BURST SIGNAL DETECTION CIRCUIT

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ABSTRACT OF THE DISCLOSURE

A burst signal detection circuit for detecting the arrival of a burst-like signal, which can accurately detect the burst signal against the DC level variations even in the case where the arriving burst signal is a weak signal, is disclosed. A DC variation removing circuit detects the bottom level of an input signal and removes the DC level variation of the input signal based on the bottom level. An amplitude identifying circuit detects the presence or absence of a burst signal based on the output signal of the DC variation removing circuit. The amplitude identifying circuit includes an amplitude detection circuit for detecting the maximum amplitude of the output signal of the DC variation removing circuit, a threshold level control circuit for controlling the threshold level and a comparator for comparing the output level of the amplitude detection circuit with the threshold level and outputting a detection signal indicating the presence or absence of a burst signal.